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APPLICANTS

James W. Cady, Austin, TX;

James Wilder, Austin, TX;

David L. Roper, Austin, TX; James Douglas Wehrly JR., Austin, TX;

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** CONTINUING DATA *****

This application is a CIP of PCT/US03/29000 09/15/2003
 which is a CON of 10/453,398 06/03/2003
 which is a CIP of 10/005,581 10/26/2001 PAT 6,576,992

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** FOREIGN APPLICATIONS *****

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 06/15/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 34	TOTAL CLAIMS 10/14	INDEPENDENT CLAIMS 13
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature <i>gbe</i>	Initials <i>gbe</i>	

ADDRESS

J. Scott Denko
 Andrews Kurth, LLP
 Suite 1700
 111 Congress Ave.
 Austin, TX
 78701

TITLE

Integrated circuit stacking system and method

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)